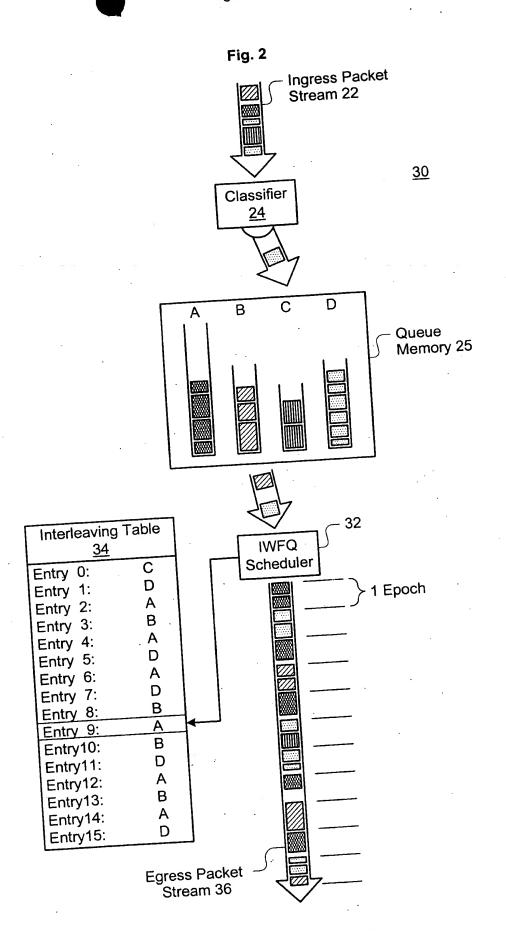
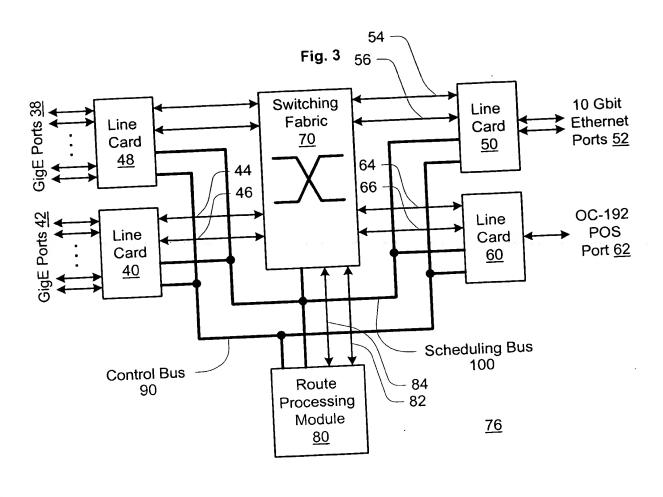
3981-3 Page 1 of 9 Fig. 1 (Prior Art) Ingress Packet Stream 22 <u>20</u> Classifier <u>24</u> C D В Queue Memory 25 3/10 1/10 2/10 Link Utilization Ratios→ 4/10 26 WRR Scheduler Queue A: 4x N bytes Egress Packet Stream 28 1 Round-Robin Queue D: 3x N bytes Cycle Queue C: 1x N bytes Queue B: 2x N bytes





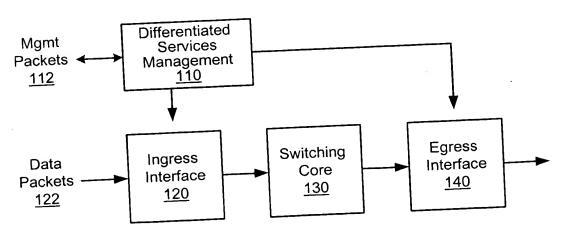
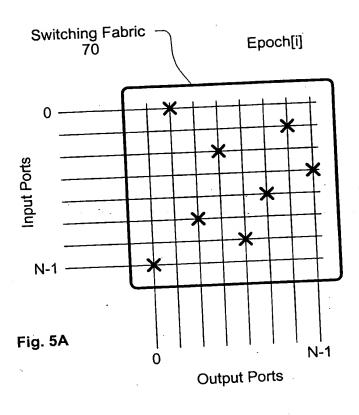
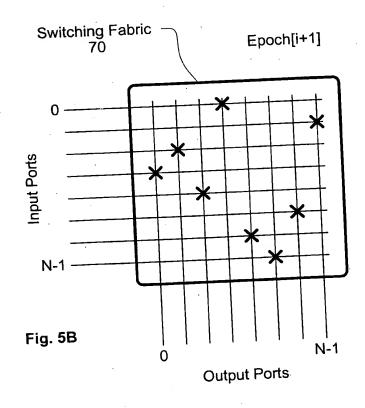
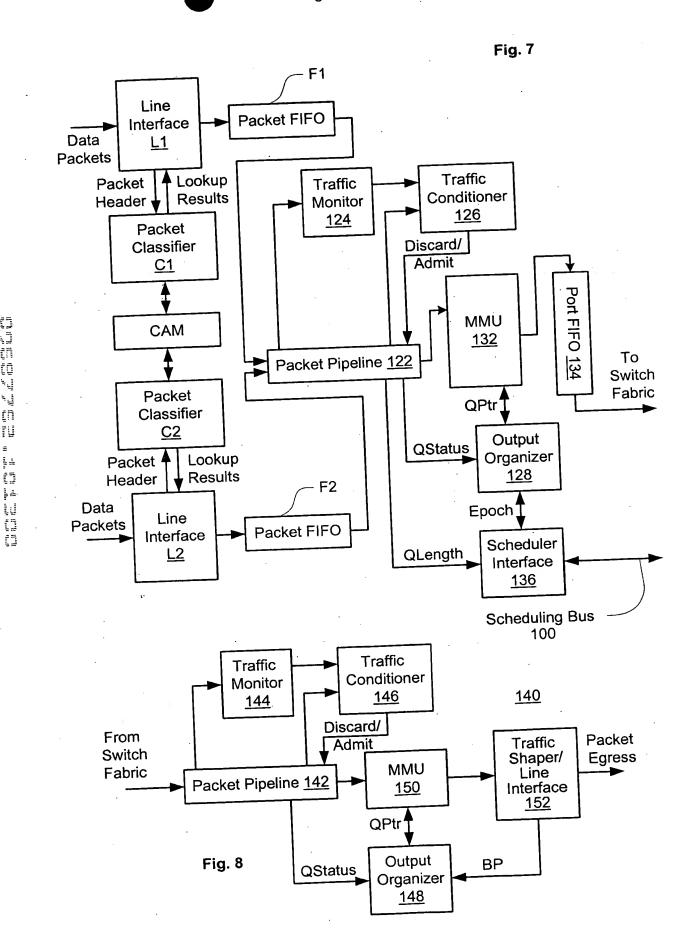


Fig. 4

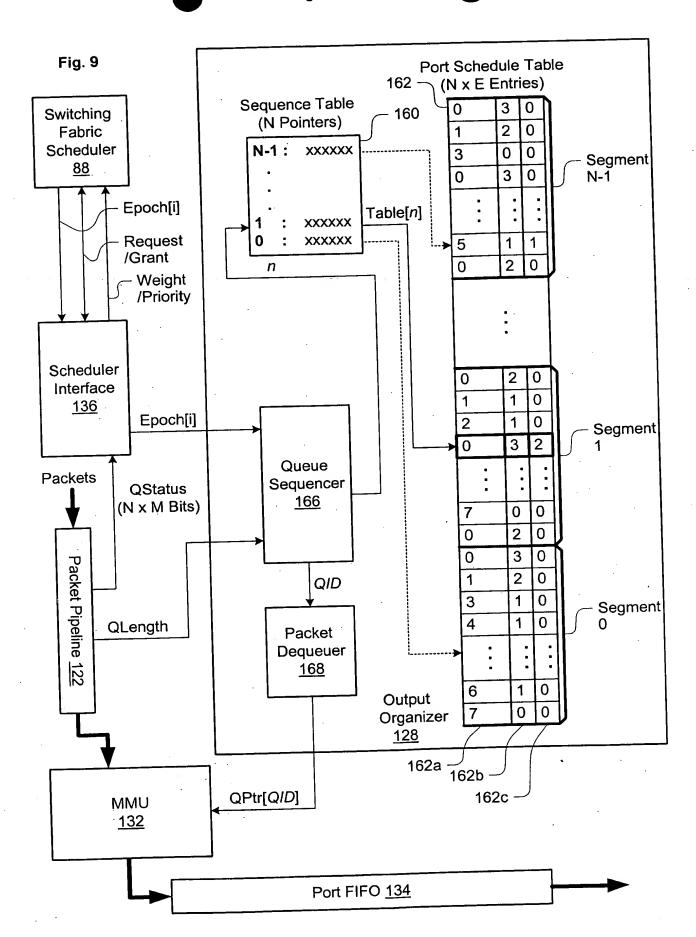






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